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PATENT NUMBER and
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U.S. UTILITY Patent Application

APPL. NUM 10049947	FILING DATE 07/23/2002	CLASS 439	SUBCLASS 79	GAU 2833	EXAMINER Leon
**APPLICANTS: Vanbesien Johan;					
**CONTINUING DATA VERIFIED: This application is a 371 of PCT/IB00/01377 08/21/2000 <i>ok FAL</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>ok FAL</i> GERMANY 109 39 064.1 08/20/1999					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO KSN0026	
Verified and Acknowledged Examiner's initials <i>FAL</i>					
TITLE : Component for assembly on a printed circuit board					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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